

Chapter 16

Motorola MC68HC11 Family MCU Architecture

Lesson 7

System Timing Device – RTI

Port A

Port B

Port C

Port D

Port E

TCNT

Out-compare

In Capture

RTC

PACNT

SCI

SPI

AMUX

S/H

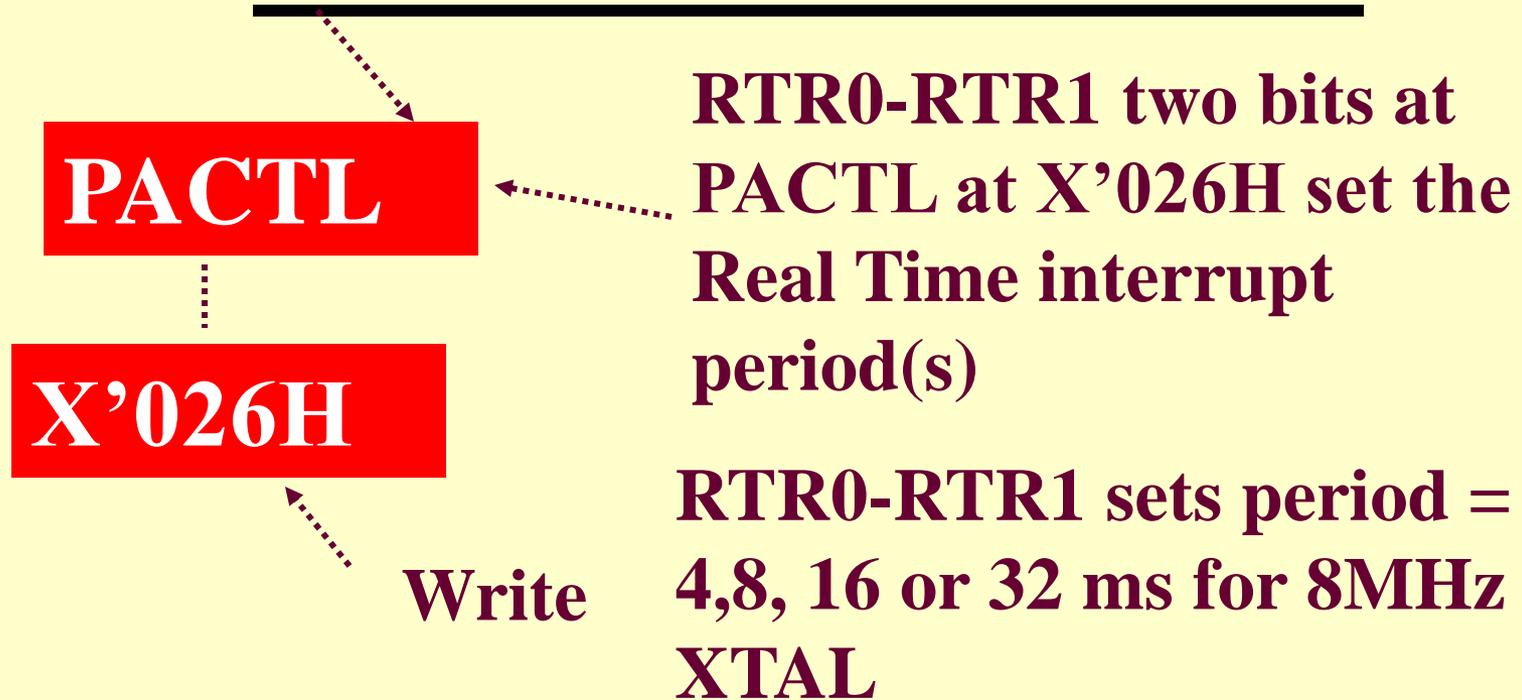
ADC

COP

Internal Devices

Real Time Interrupt Periods and Interrupt Mask RTI set bit at TMSK2

Setting periods of Real Time Interrupts RTI (Real Time Interrupts)



X' four bits are as per init register



X' four bits are as per init register

RTI Interrupt Flag

TFLG2

X'025H

Read

**RTIF bits at
TFLG2 at
0025H**

X' four bits are as per init register

Summary

We learnt

- Real Time Interrupts
- **RTR0-RTR1 sets period = 4,8, 16 or 32 ms for 8MHz XTAL**
- **RTI mask bit**
- **RTIF Flag bit (sets on RTI occurrence)**

End of Lesson 7 on System Timing Device – RTI