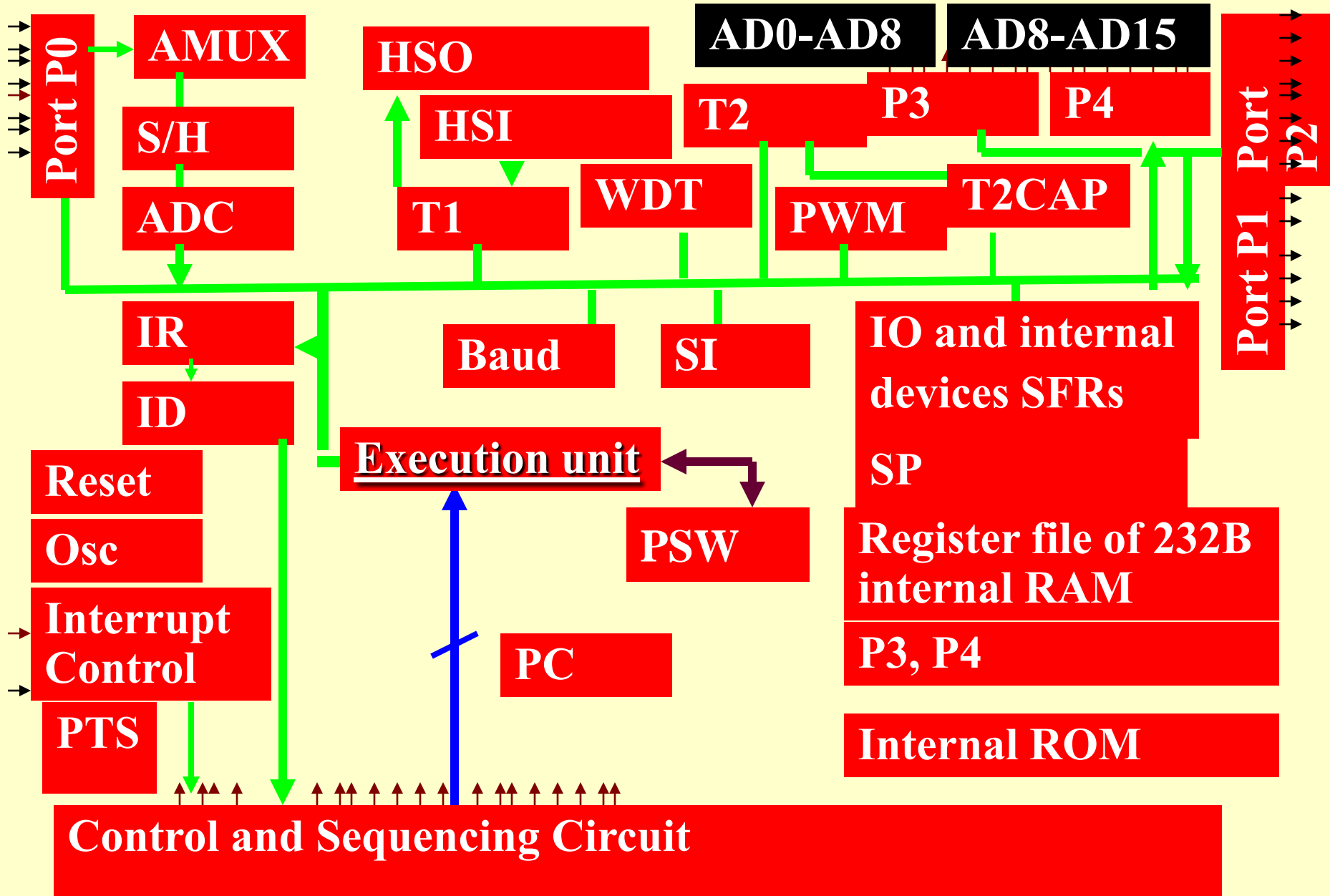


Chapter 14

80x96 Family Microcontrollers



Lesson 6

80x96 Peripheral Transactions Server

Peripheral server enable (PSE) bit

- A PSW bit enables or disables the DMA operations

PTS Signals

- Used for direct memory access by external peripherals and perform input-out transactions between memory and peripheral)
- Hold: Request from external peripheral to hold use of buses (address and data buses)
- HLDA: Request acknowledgement when granted

Port P1

Address – 0FH

P1.7
P1.6
P1.5
P1.4
P1.3
P1.2
P1.1
P1.0

Hold
HLDA ↘ PTS
 ↗
IO
IO or PWM1
IO or PWM2
IO
IO
IO

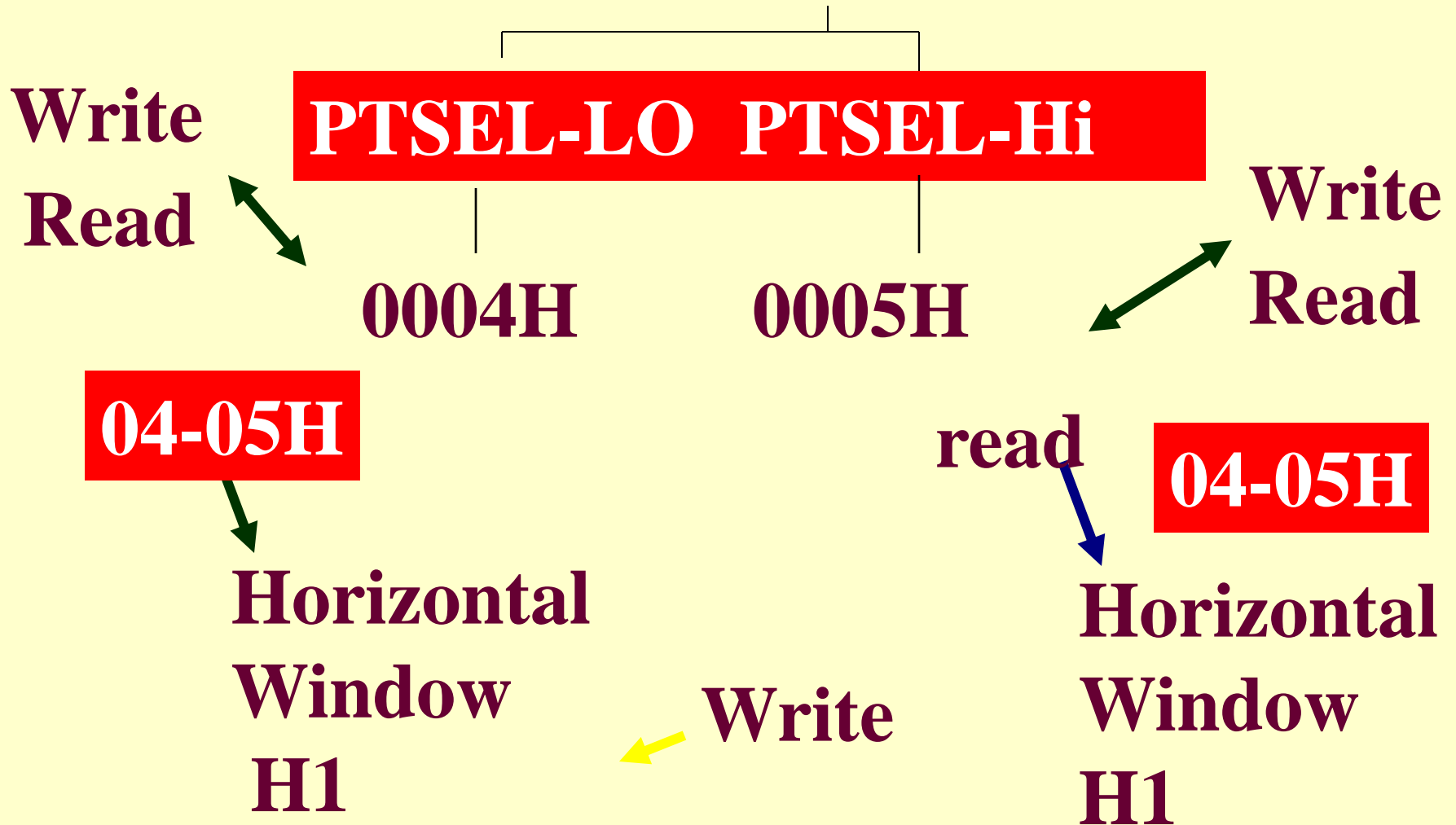
PTS:
Peripheral
Transactions
Server
function as
DMA

Option 3

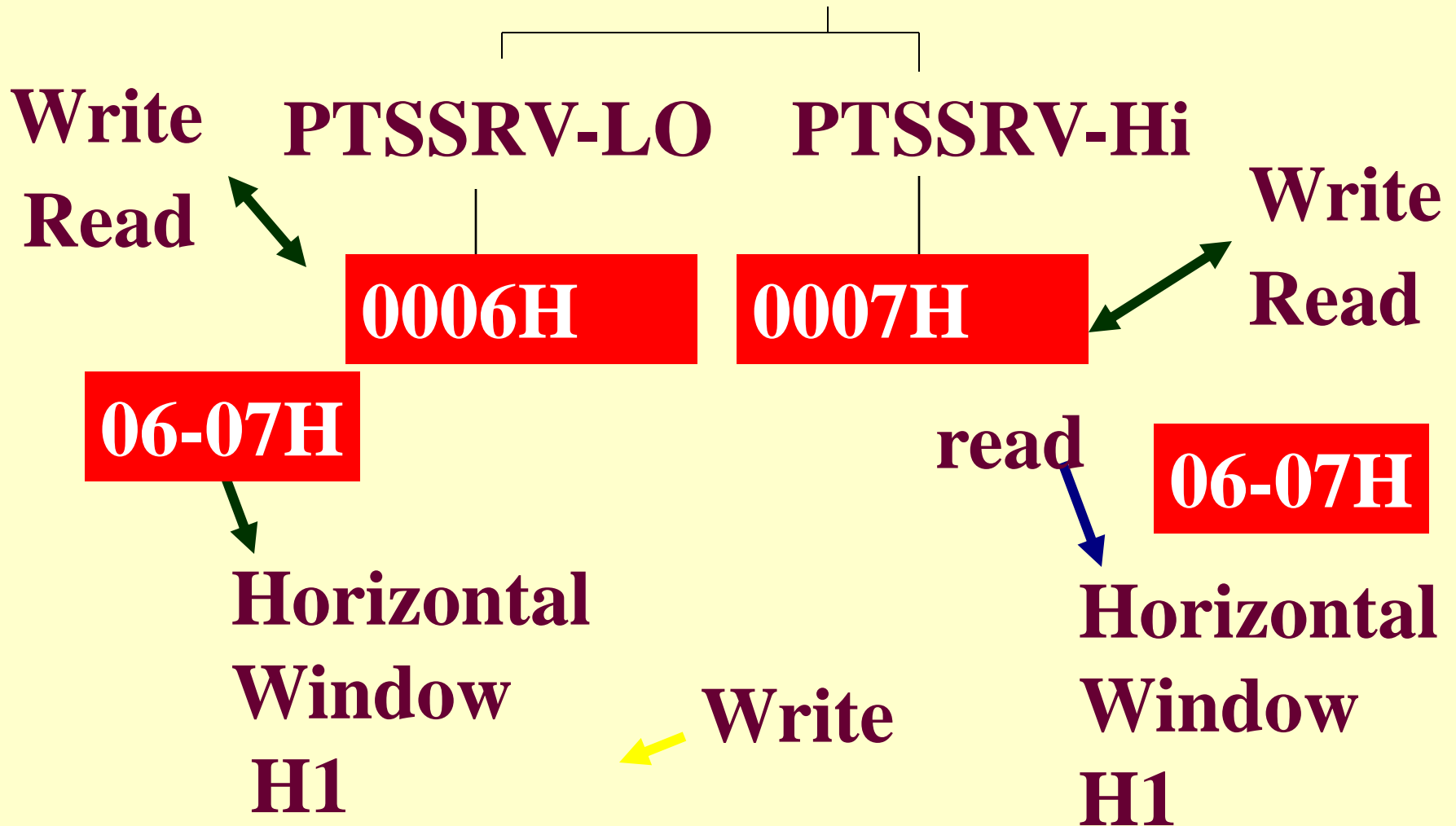
Horizontal window 1 (read) cum (write) PTS SFR Addresses

- PTSSEL_HO - PTSSEL_LO 16-bit registers to select peripheral transactions [PTSEL-Peripheral Transaction Server Service Channels enable/disable bits]
- PTSSRV_HO- PTSSRV_LO 16-bit registers to service peripheral transactions [PTSSRV-Peripheral Transaction Server End of Service/Service Pending Flag bits]

PTSEL Addresses



PTSSRV Addresses



Summary

We learnt

- Peripheral Transaction Server
Signals and SFR Addresses

End of Lesson 6 on
80x96 Peripheral Transactions
Server